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May 25, 2005

Mail Stop AF
Commissioner for Patents
P. O. Box 1450
Alexandria, VA 22313-1450

Re: Applicant(s): Attila Kovacs-Birkas
Assignee: NEC Electronics, Inc.
Title: CELL MODELING IN THE DESIGN OF AN INTEGRATED
CIRCUIT
Application No.: 09/878,497
Filing Date: June 11, 2001
Group Art Unit: 2123
Examiner: William D. Thomson
Attorney Docket No.: NEC0239US

Dear Sir:

Transmitted herewith are the following documents in the above-identified application:

- (1) Return Receipt Postcard
- (2) This Transmittal Letter (1 page, in duplicate)
- (3) Response To Final Office Action (10 pages)



No additional fee is required.



The fee has been calculated as shown below:

CLAIMS AS AMENDED

	Claims Remaining <u>After Amendment</u>		Highest No. Previously <u>Paid For</u>		Present <u>Extra</u>	<u>Rate</u>	Additional <u>Fee</u>
Total Claims	23	Minus	23	=	0	x \$50.00	\$ 0.00
Independent Claims	3	Minus	3	=	0	x \$200.00	\$ 0.00



Fee of _____ for the first filing of one or more multiple
dependent claims per application

\$



Fee for Request for Extension of Time

\$

120.00

Total additional fee for this Amendment:

\$

120.00



Conditional Petition for Extension of Time: If an extension of time is required for timely
filing of the enclosed document(s) after all papers filed with this transmittal have been
considered, an extension of time is hereby requested.



Please charge our Deposit Account No. 502306 in the amount of

\$

120.00



Also, charge any additional fees required and credit any overpayment to our Deposit
Account No. 502306.

Total:

\$

120.00

I hereby certify that this correspondence is being deposited with
the United States Postal Service as First Class Mail in an envelope
addressed to: Mail Stop AF, Commissioner for Patents, P. O. Box
1450, Alexandria, Virginia, 22313-1450, on 5/25/05.

Attorney for Applicant(s)

5/25/05

Date of Signature

Respectfully submitted,

Eric A. Stephenson
Attorney for Applicant(s)
Reg. No. 38,321



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Attila Kovacs-Birkas
Assignee: NEC Electronics, Inc.
Title: CELL MODELING IN THE DESIGN OF AN INTEGRATED CIRCUIT
Application No.: 09/878,497 Filed: June 11, 2001
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Docket No.: NEC0239US Confirmation No.: 3073

Austin, Texas
May 25, 2005

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RESPONSE TO FINAL OFFICE ACTION

Dear Sir:

This paper is responsive to the Final Office Action dated February 24, 2005, having a shortened statutory period expiring on May 24, 2005. Further examination and reconsideration are respectfully requested in view of the amendments and remarks set forth below.

Remarks/Arguments begin on page 7 of this paper.